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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MANFRED HORSTMANN  
ROLF STEPHAN  
KARSTEN WIECZOREK  
STEPHAN KRUEGEL

Serial No.: 09/847,622

Filed: May 2, 2001

For: FIELD EFFECT TRANSISTOR WITH  
REDUCED GATE DELAY AND METHOD  
OF FABRICATING THE SAME

Patent No. 6,798,028 B2

Issue Date: September 28, 2004

Group Art Unit: 2815

Examiner: Jesse A. Fenty

Att'y Docket: 2000.064200/DE0053

Customer No.: 23720

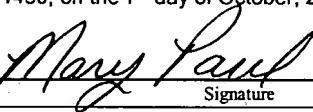
Certificate  
OCT 19 2004  
of Correction

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the 7<sup>th</sup> day of October, 2004.

  
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The patentee requests a certificate of correction for Patent and Trademark Office ("PTO") errors pursuant to 37 C.F.R. § 1.322. Because these errors were the PTO's, no fee should be required. If any fee is required, please charge Advanced Micro Devices, Inc. Deposit Account No. 01-0365/DE0053.

The following errors are listed on the attached form PTO-1050.

Col. 8, line 47 (claim 1, line 6), "electrode" should be -- electrode --.

Col. 9, line 1 (claim 5, line 1), after "wherein" insert -- the --.

OCT 20 2004

Please date stamp and return the accompanying postcard to evidence receipt of this document.

Respectfully submitted,

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OCT 20 2004

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT: 6,798,028 B2  
DATED: SEPTEMBER 28, 2004  
INVENTORS: MANFRED HORSTMANN ET AL.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 8, line 47 (claim 1, line 6), "elctrode" should be -- electrode --.

Col. 9, line 1 (claim 5, line 1), after "wherein" insert -- the --.

MAILING ADDRESS OF SENDER:

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